

21/6/04

**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

Applicant: Urs Bapst
 Christoph Berger
 Russell Alan Budd
 Folkert Horst
 Bert Jan Offrein
 Docket No.: YOR920030633US1
 Serial No.: Unassigned
 Filing Date: Concurrently Herewith
 Group: Unassigned

U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
INITIAL					
JMB	6,236,788	05/22/01	Moisel	385/52	
JMB	20030021541	01/30/03	Carpenter	385/52	07/24/01

FOREIGN PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO
INITIAL					
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-

OTHER DOCUMENTS

EXAMINER	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
INITIAL		

JMB Miller, D.A.B., "Rationale and Challenges for Optical Interconnects to Electronic Chips," Proceedings of the IEEE, Vol. 88, No. 6, Pgs. 728-749 (June 2000).

JMB Nordin et al., "A Systems Perspective on Digital Interconnection Technology," Journal of Lightwave Technology, Vol. 10, No. 6, Pgs. 811-827 (June 1992).

J Martin Bei

Examiner

08/19/2005

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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